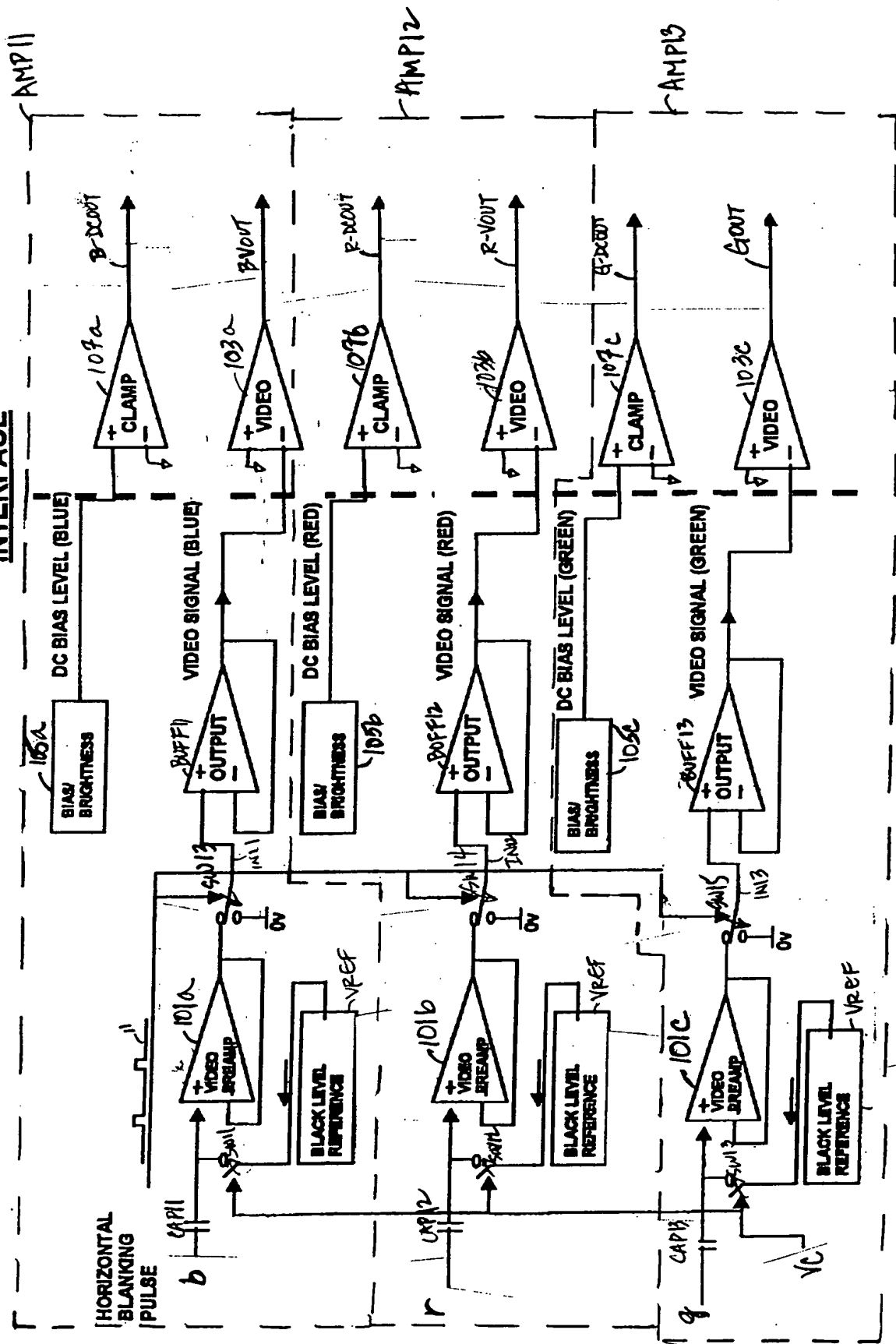


INTERFACE



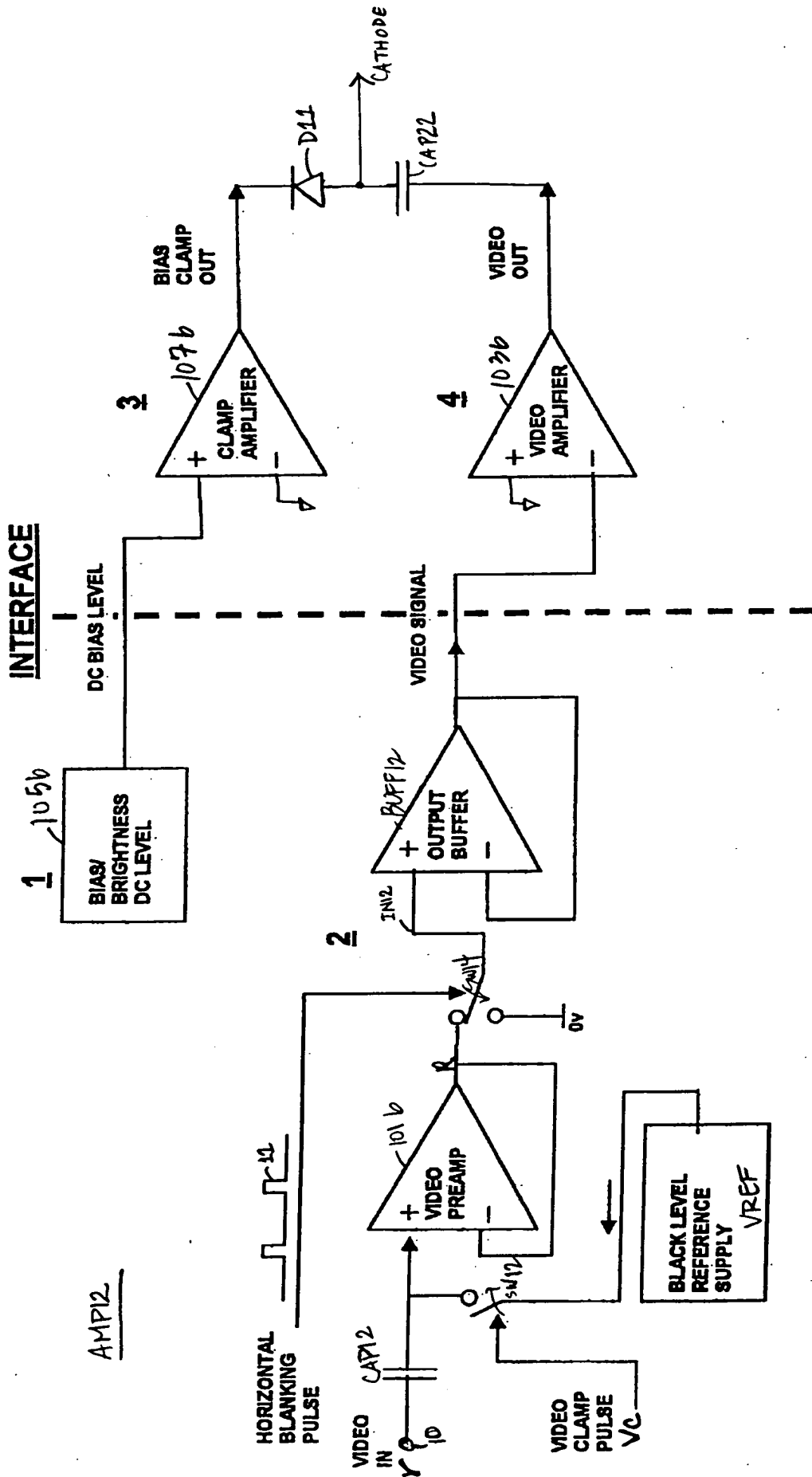


FIGURE 2

004201" 6E286960



National Semiconductor

AG2DC

TYPICAL DISPLAY VIDEO SYSTEM

ARC PROTECTION,
ESD, INPUT
TERMINATION
ETC NOT SHOWN

PRE-AMP

CRT DRIVER

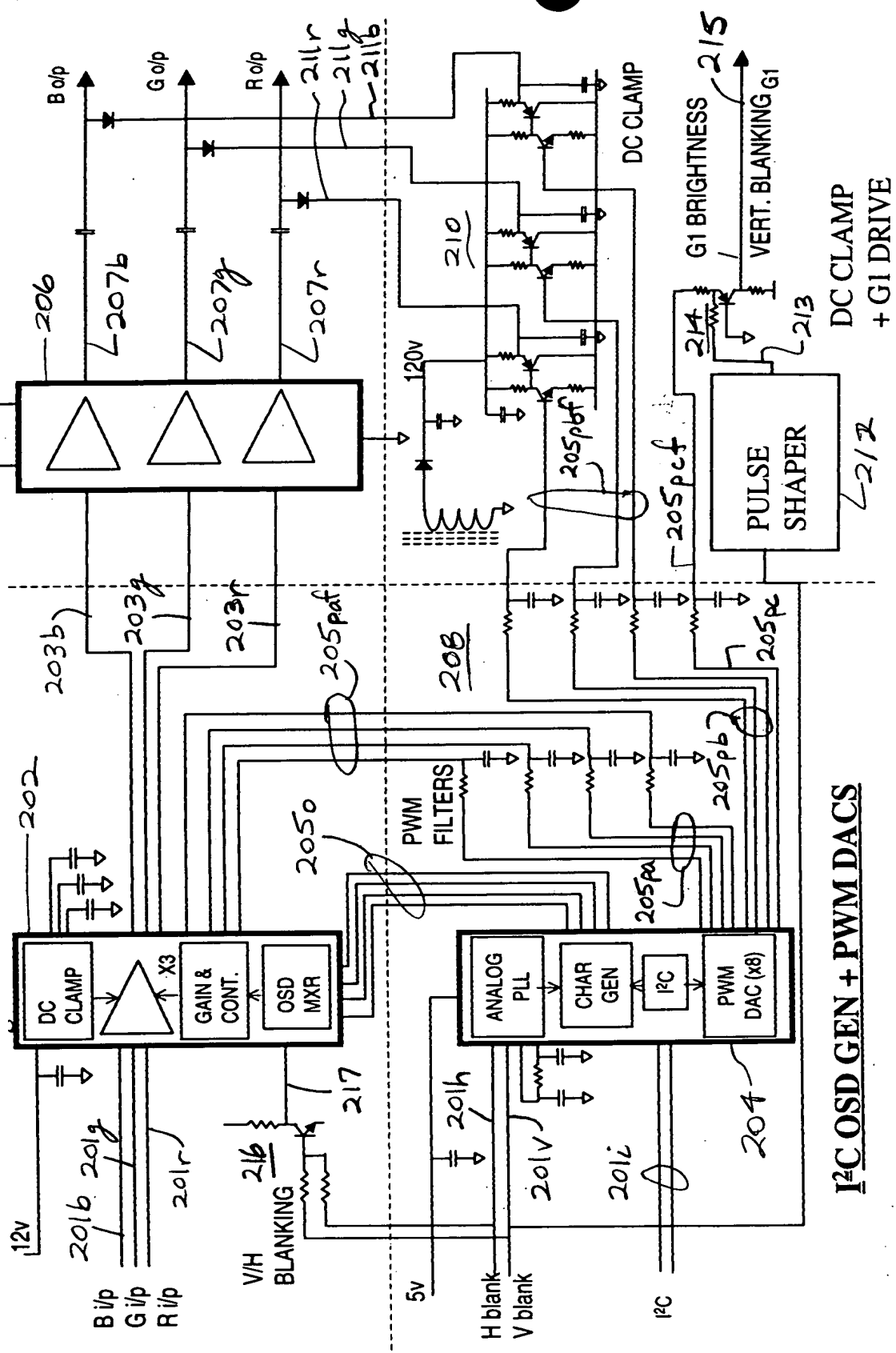


FIGURE 3 (PRIOR ART)



National Semiconductor

AG2DC

DC COUPLED VIDEO SYSTEM

ARC PROTECTION,
ESD, INPUT
TERMINATION
ETC NOT SHOWN

PRE-AMP

DC COUPLED CRT DRIVER

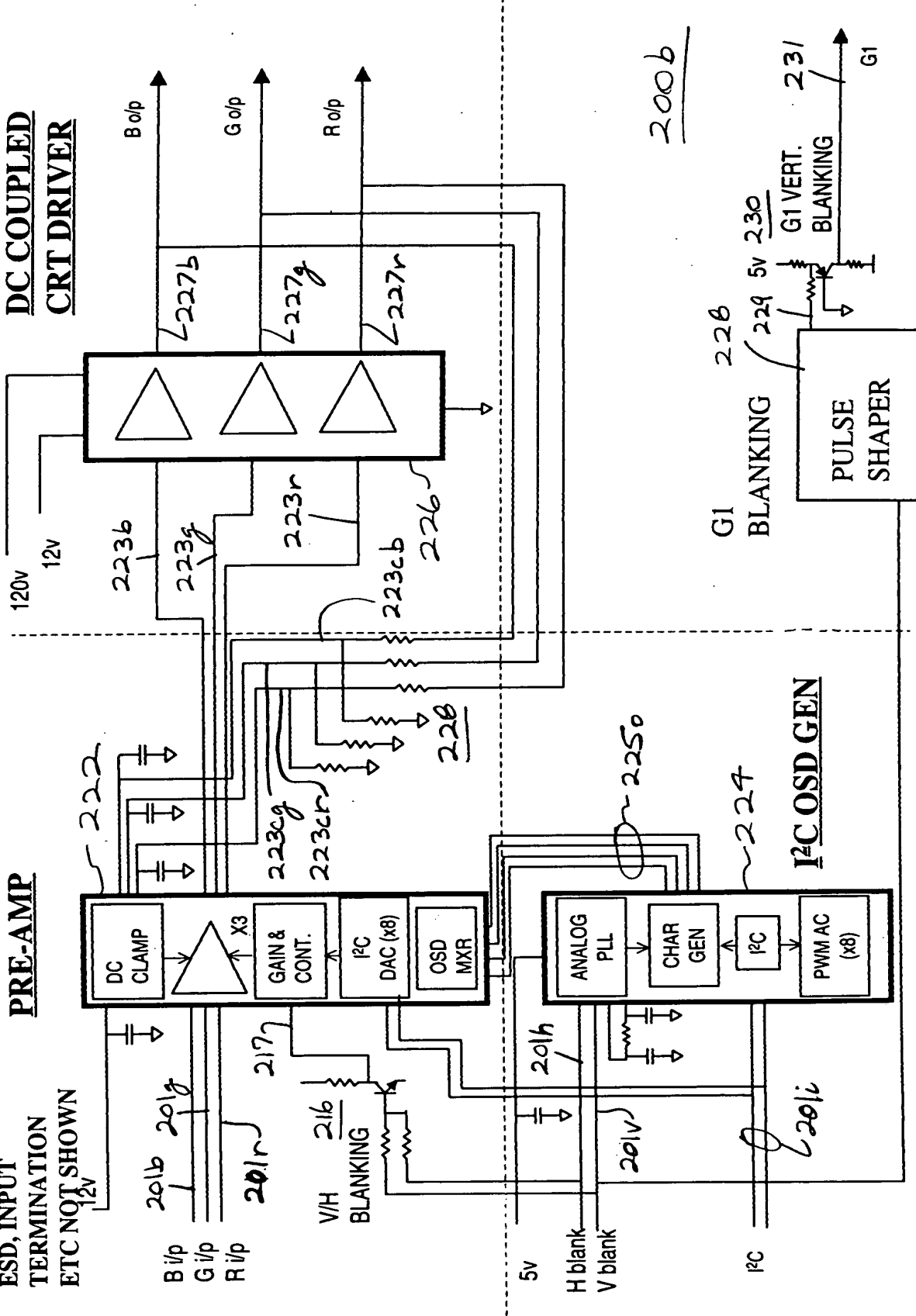


FIGURE 4 (PRIOR ART)

007201 66286960

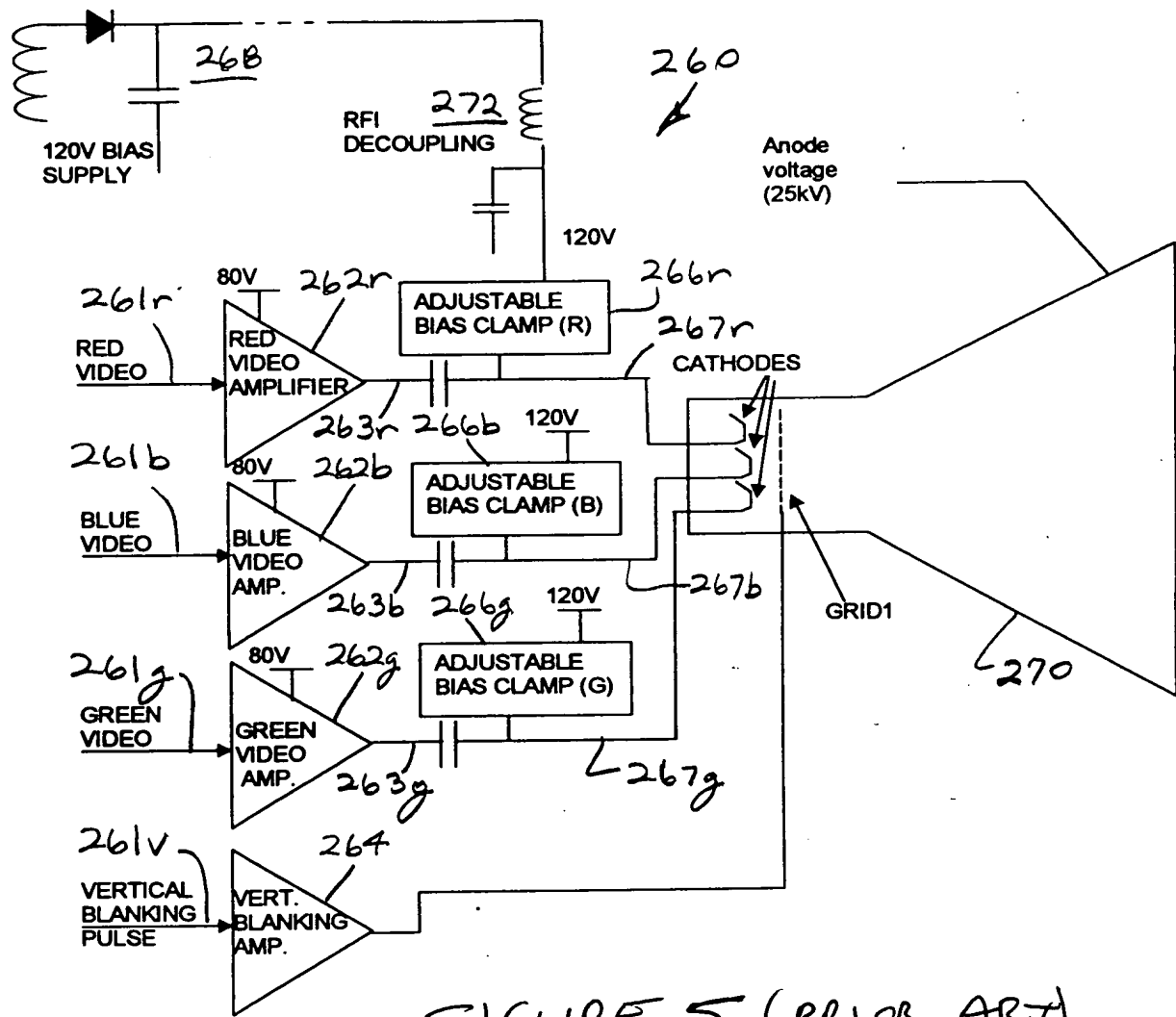


FIGURE 5 (PRIOR ART)

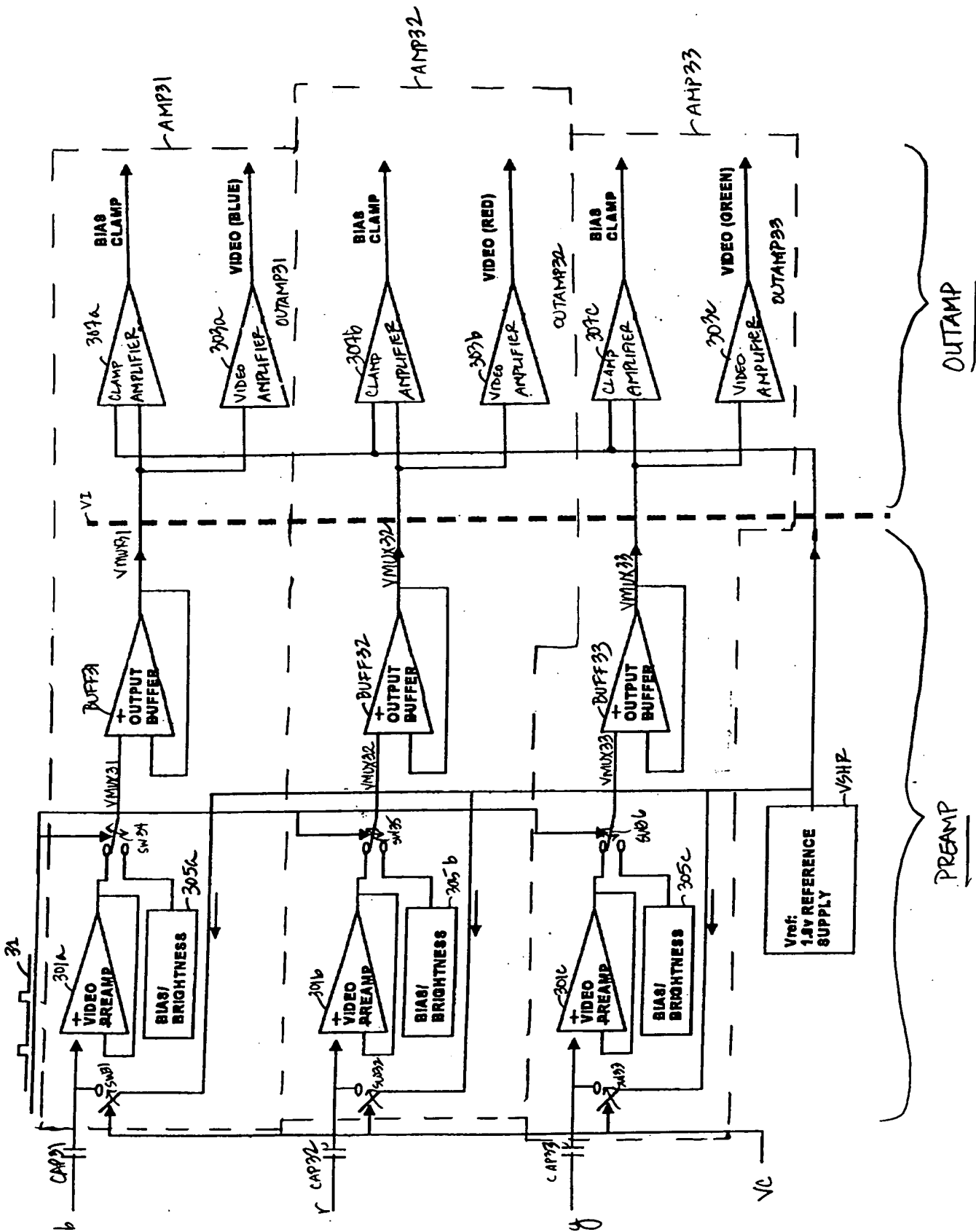
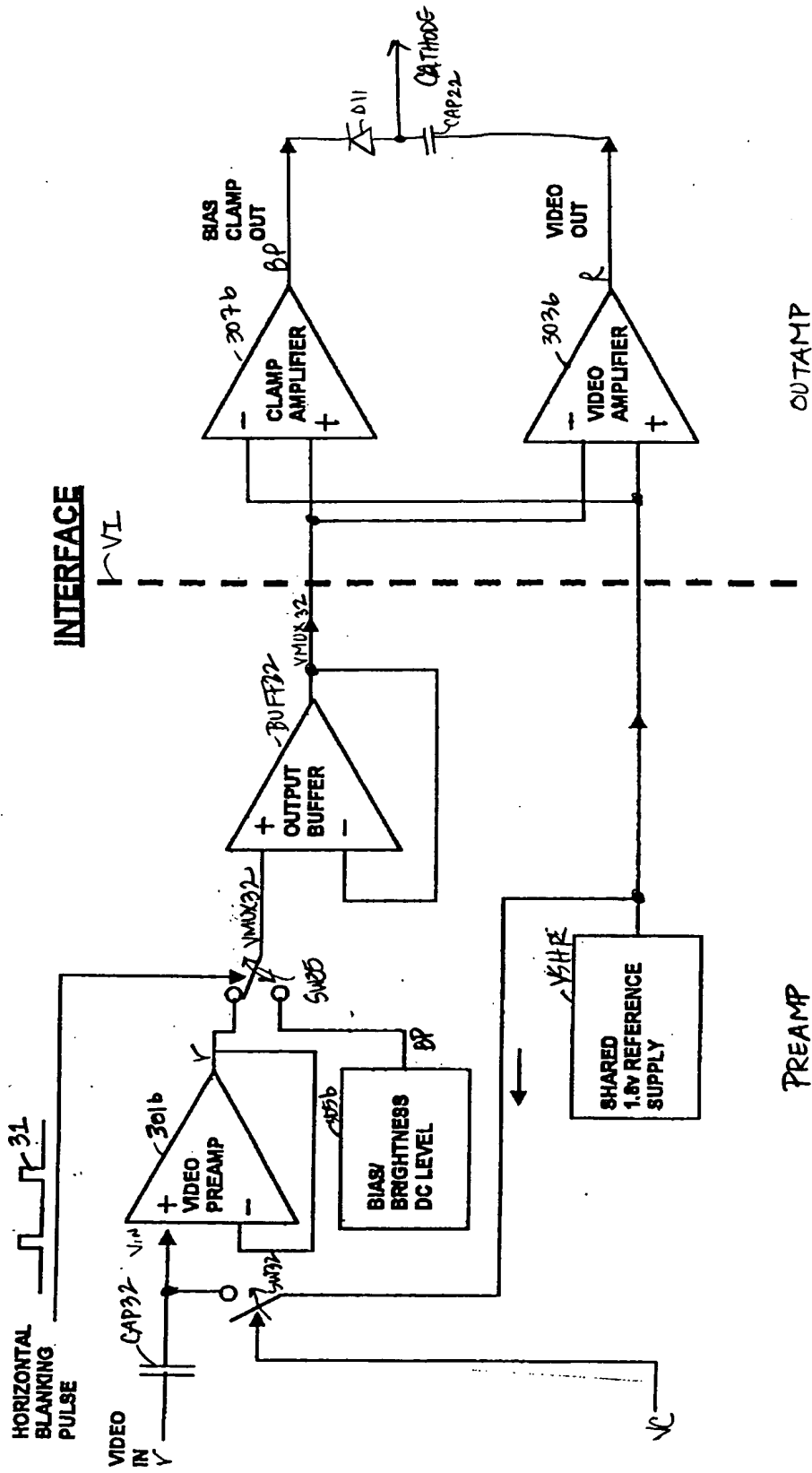


FIGURE 6



AMP32

FIGURE 7

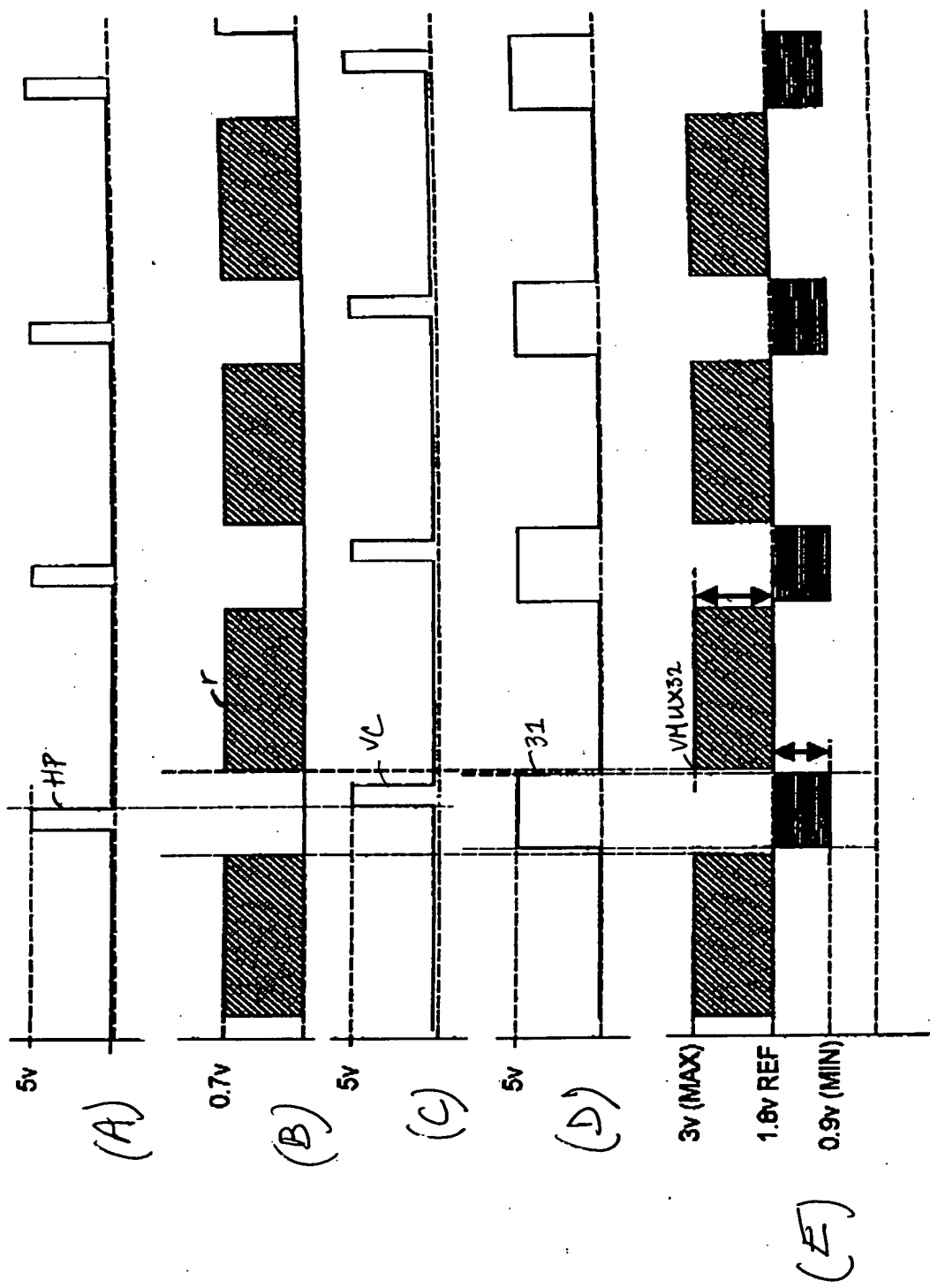


FIGURE 8

FlGurE 9

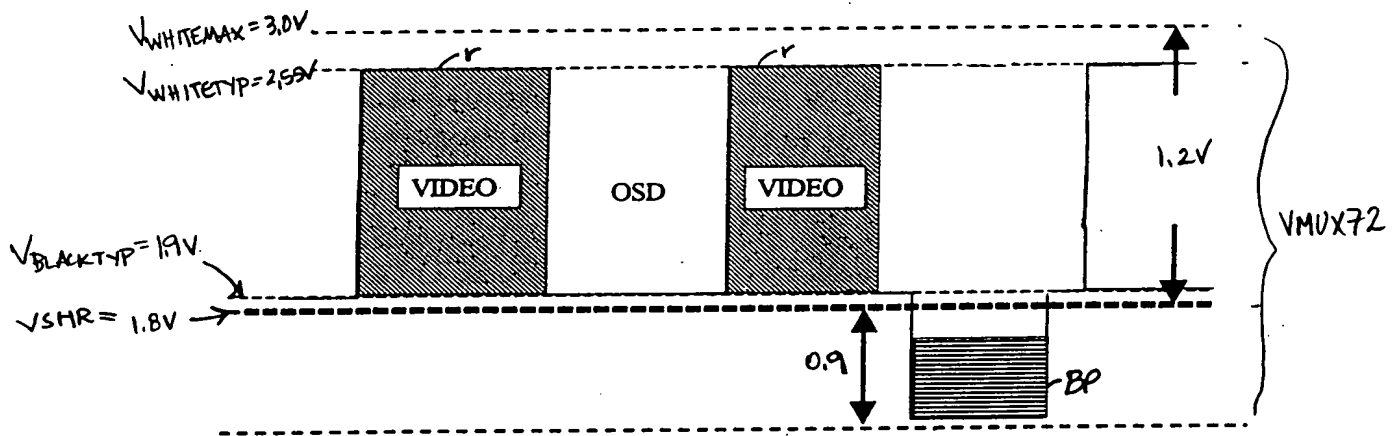


FIGURE 10

09698739 102700

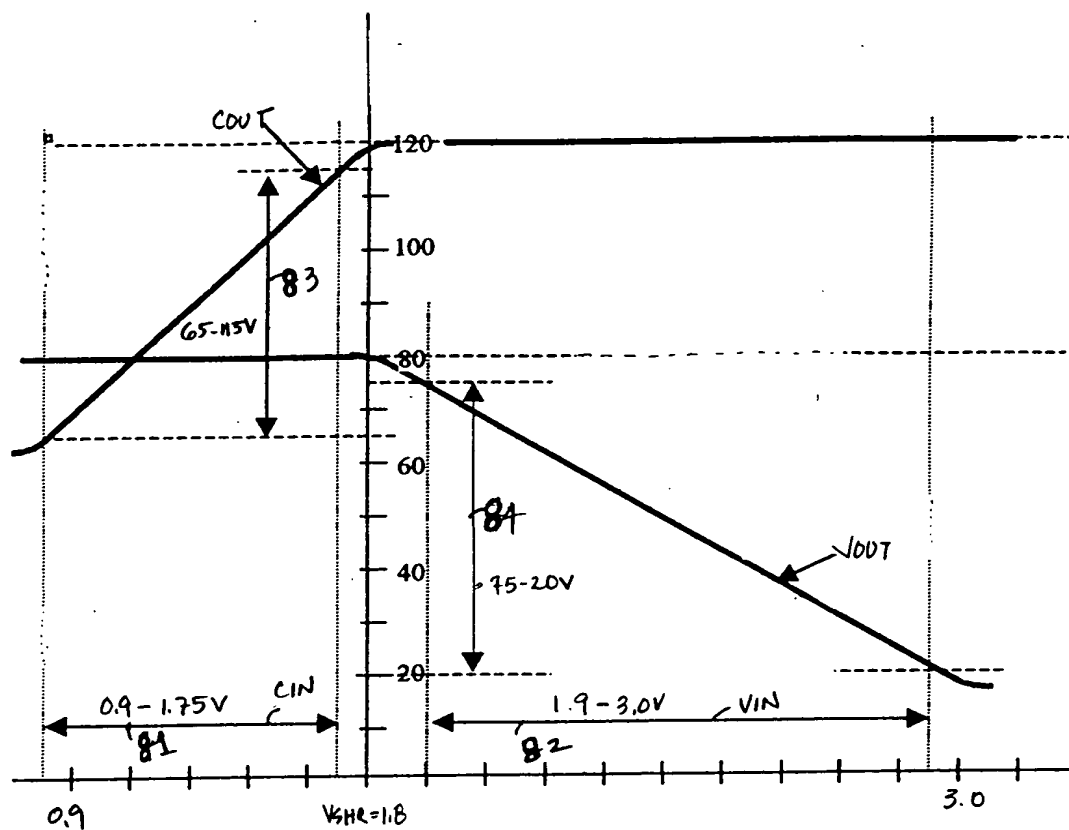


FIGURE 11

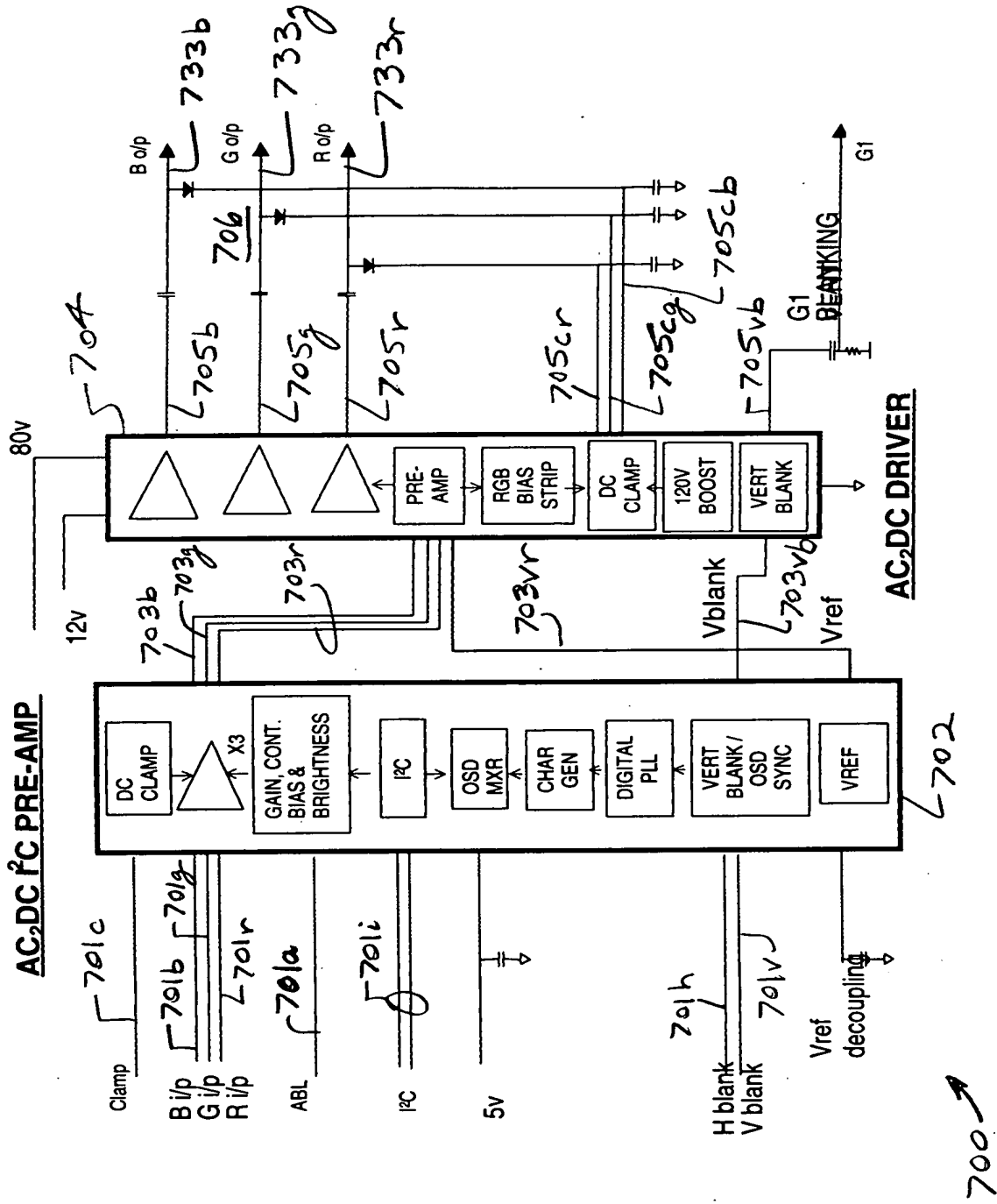
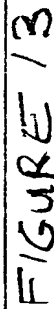


FIGURE 12





National Semiconductor

INPUT SIGNALS



OUTPUT SIGNAL

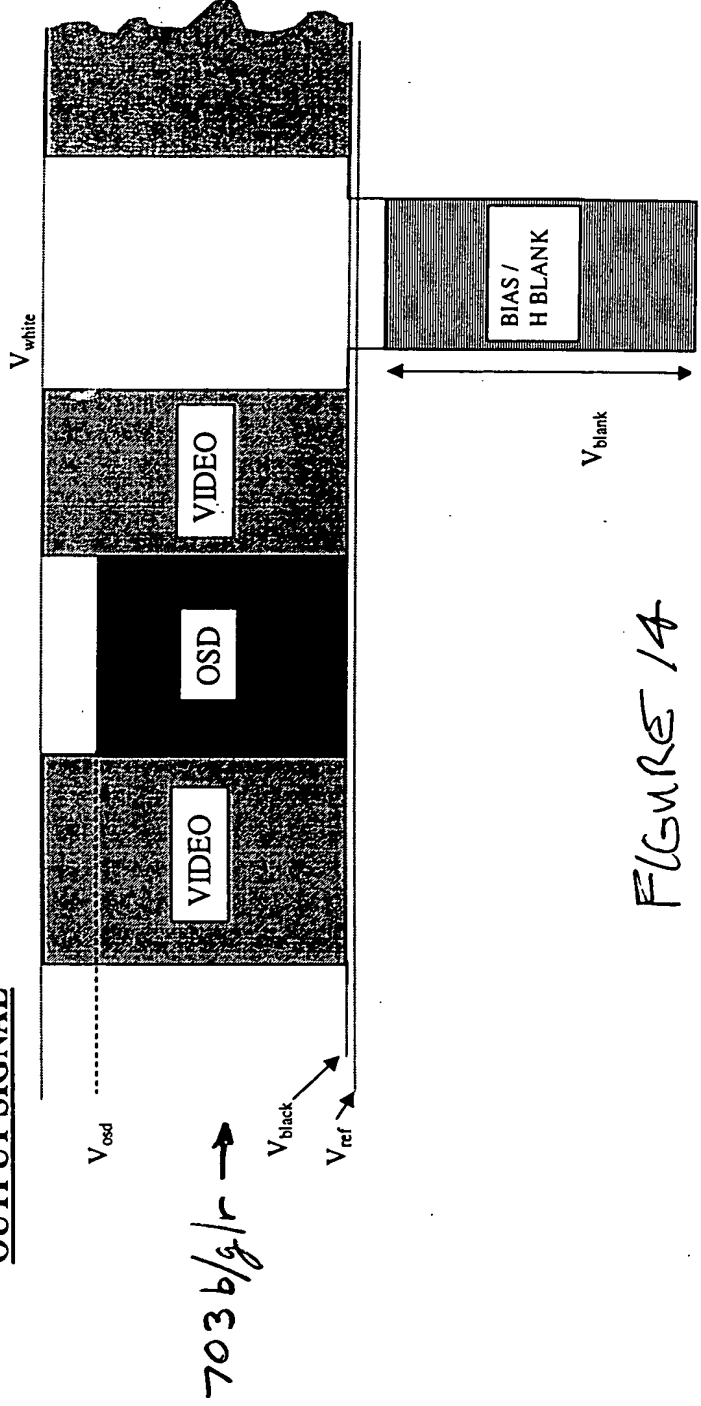


FIGURE 14

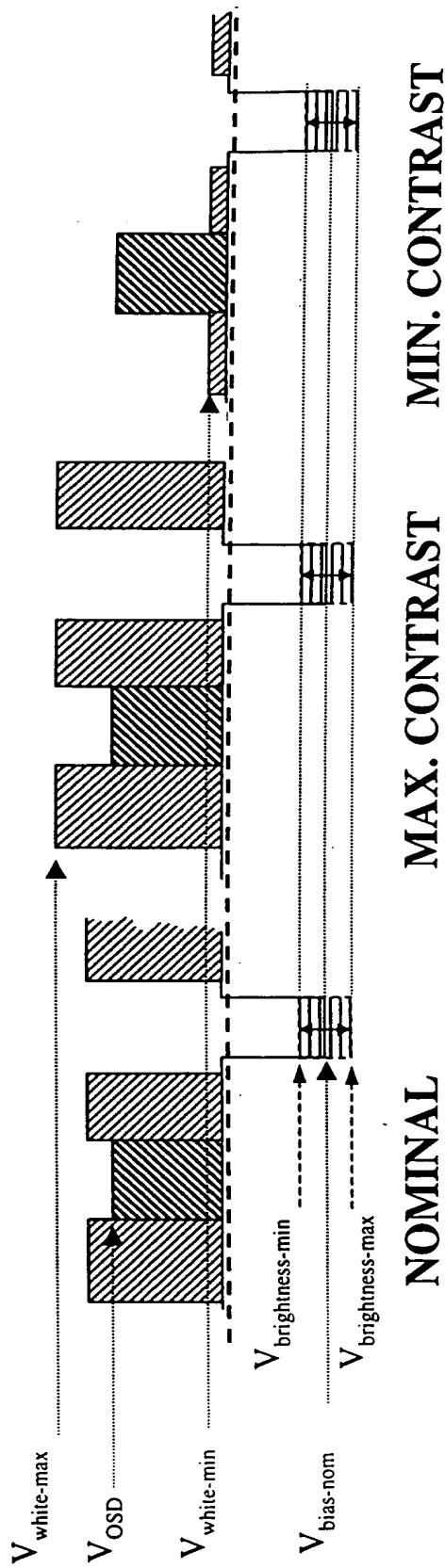


FIGURE 15

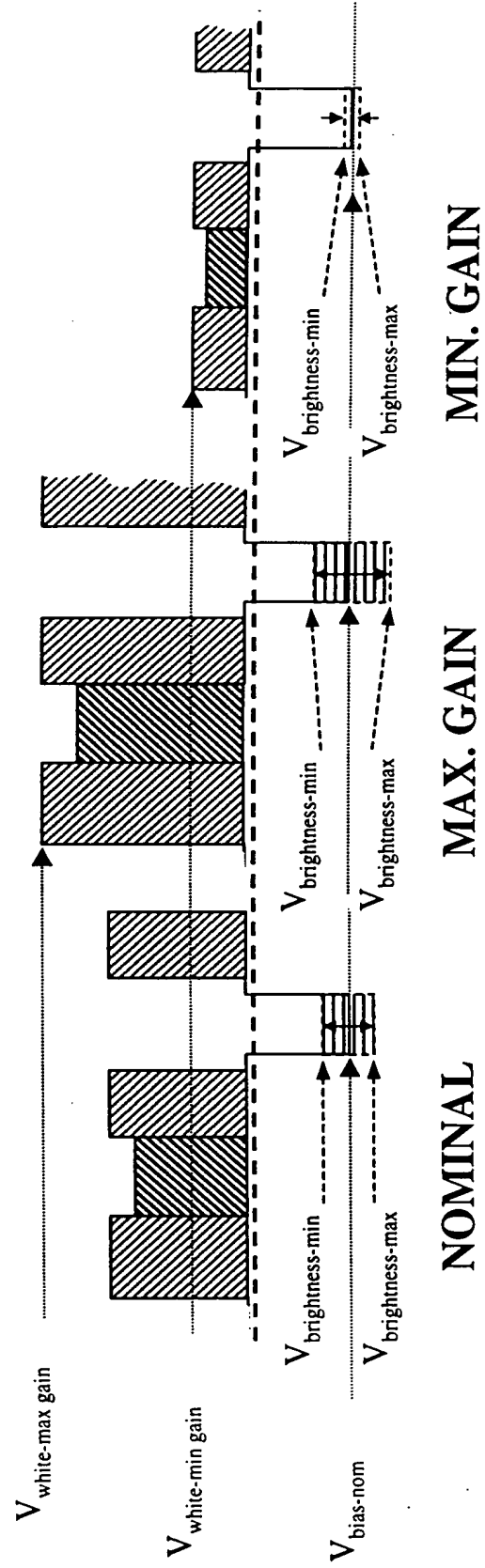


FIGURE 16

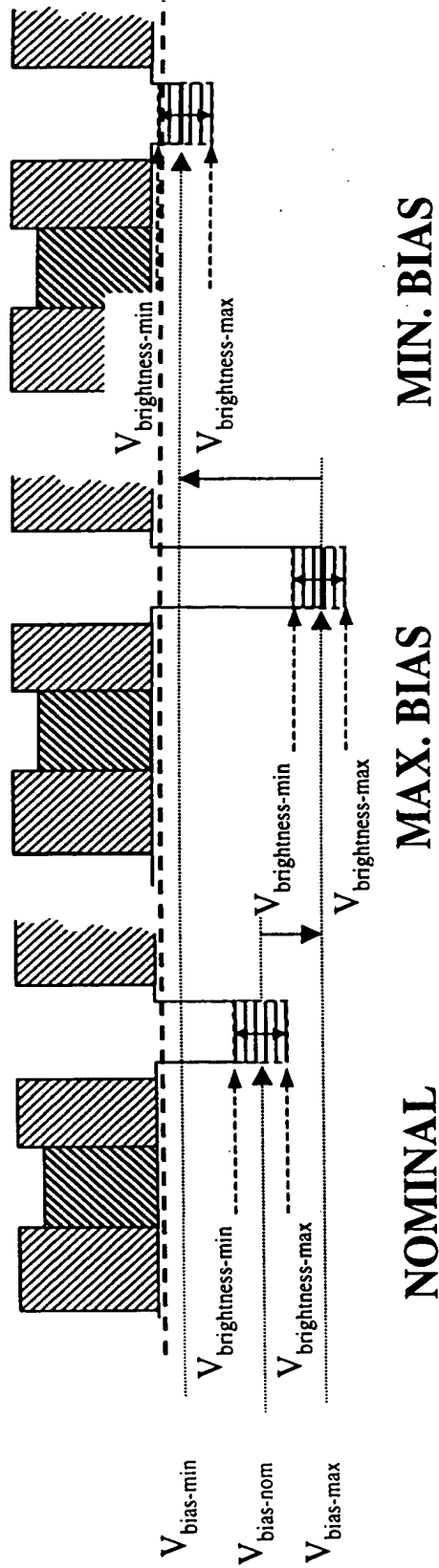


FIGURE 17

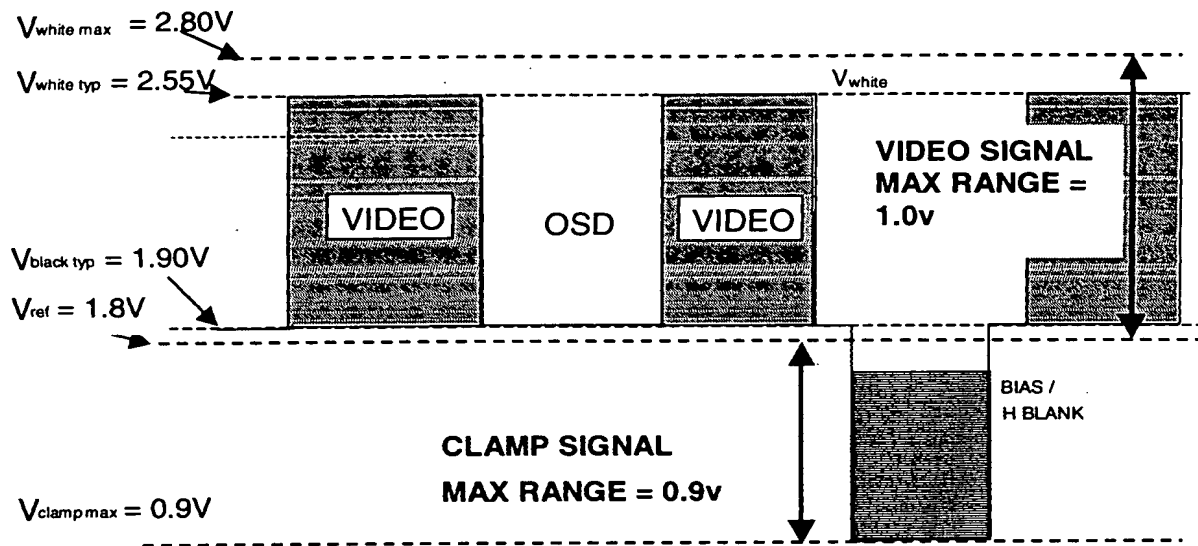
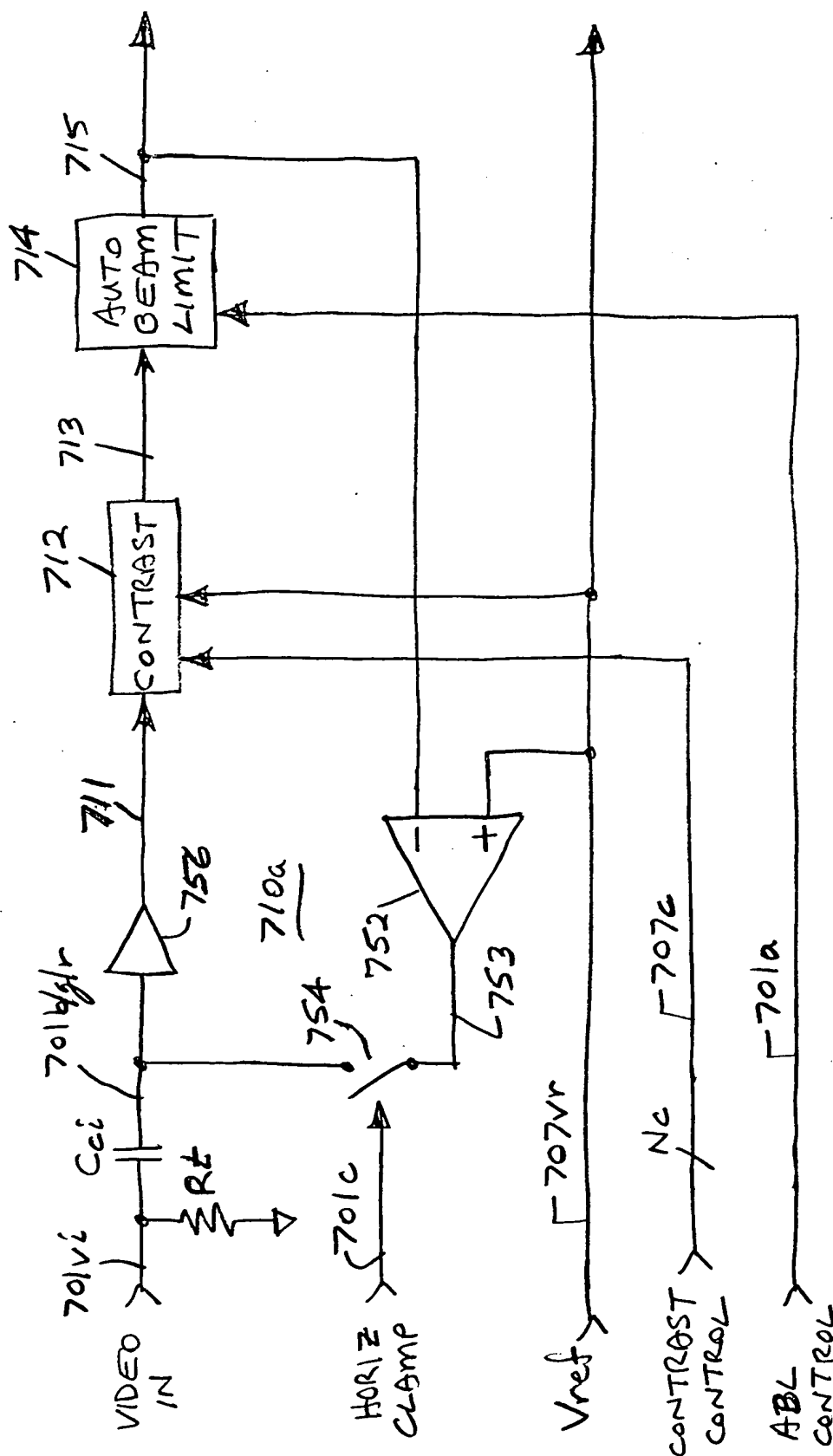


FIGURE 18

00/201" 6E/86960

7036/glr



19/GURE

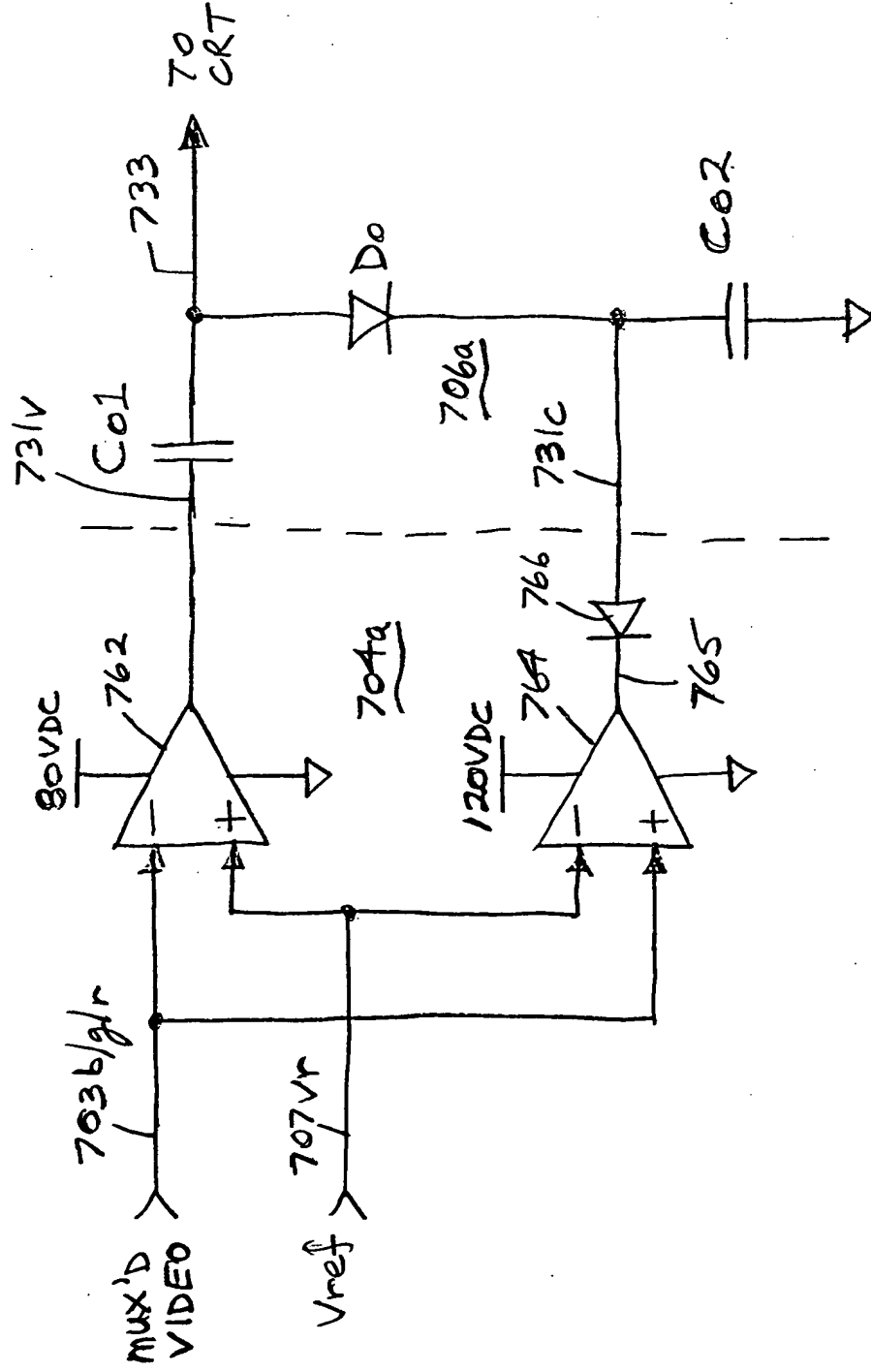
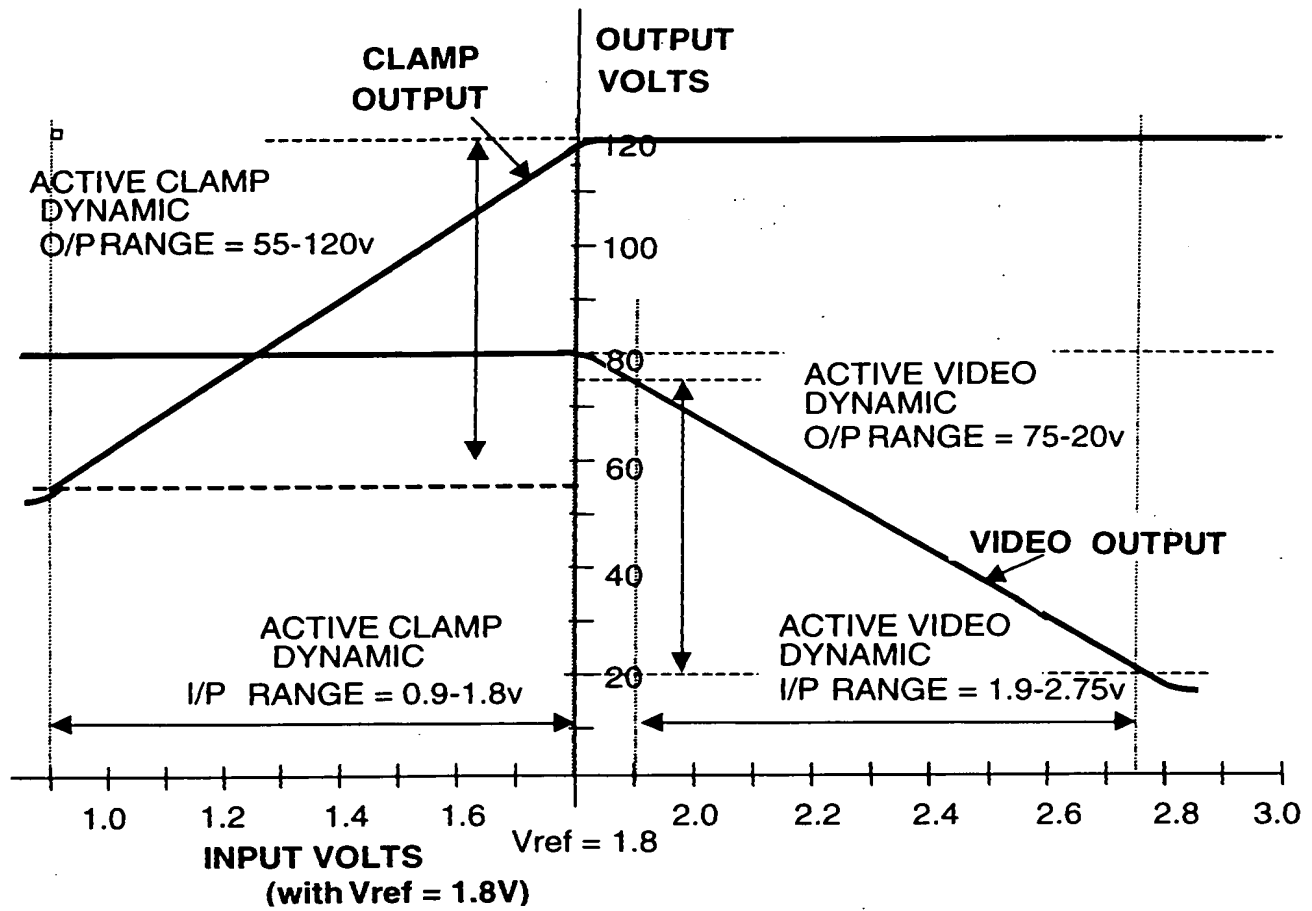


FIGURE 20



(Test Conditions: $V_{ref} = 1.8v$, $V_{cc1} = 80v$, $V_{cc2} = 120v$, $V_{bb} = 12v$)

FIGURE 21

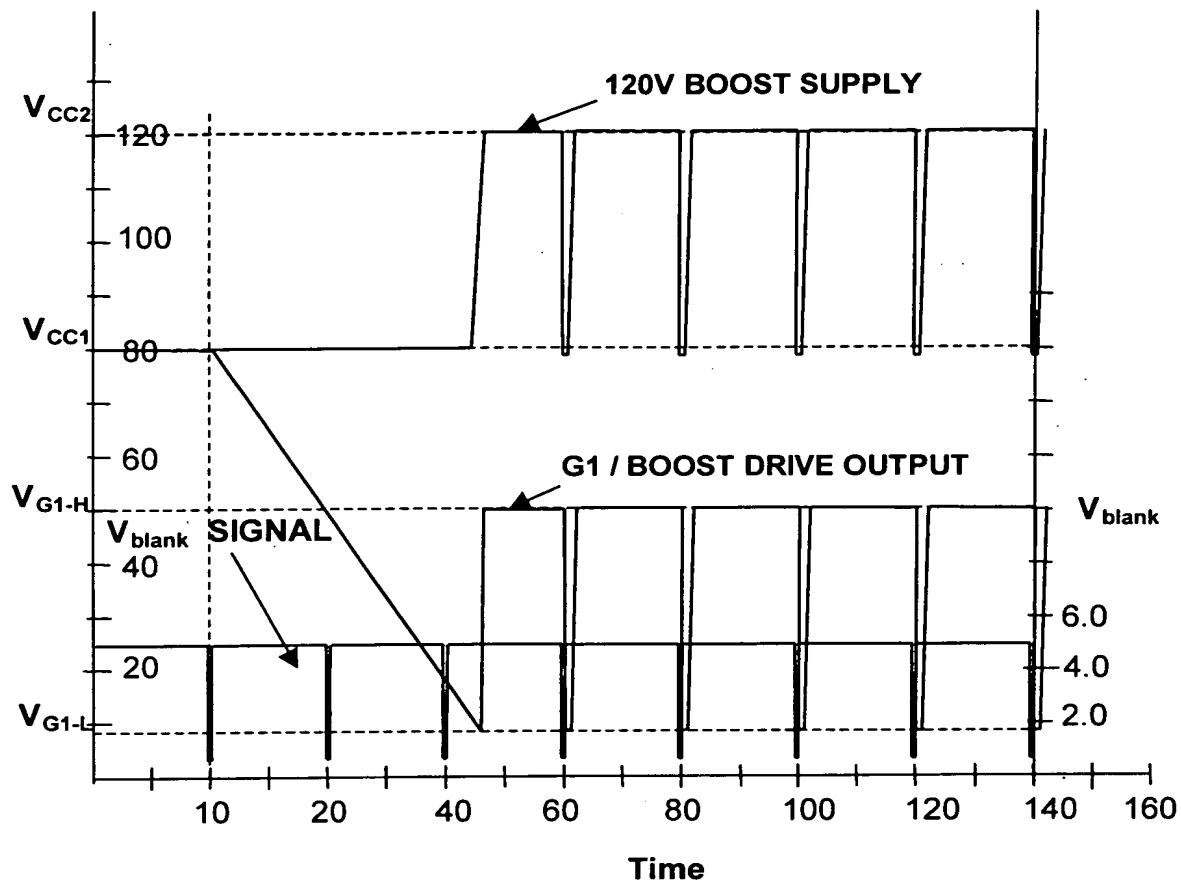


FIGURE 22

004201" 6E486960

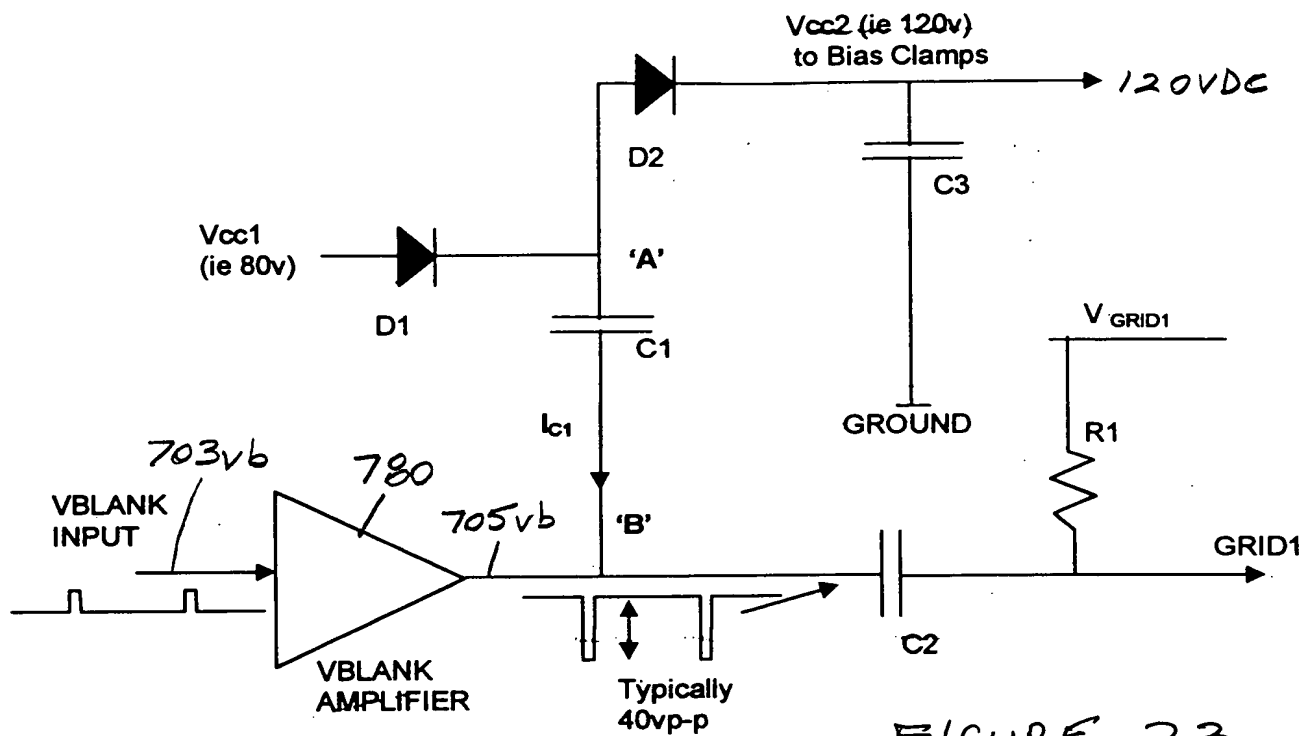
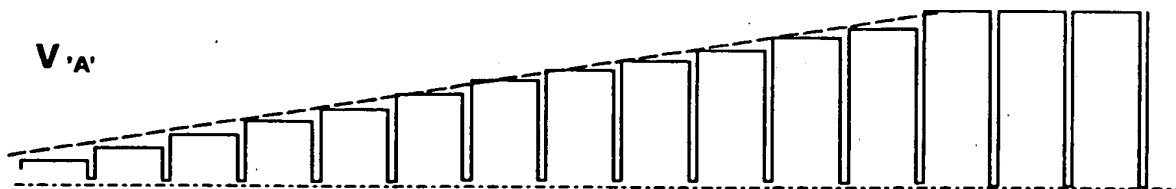


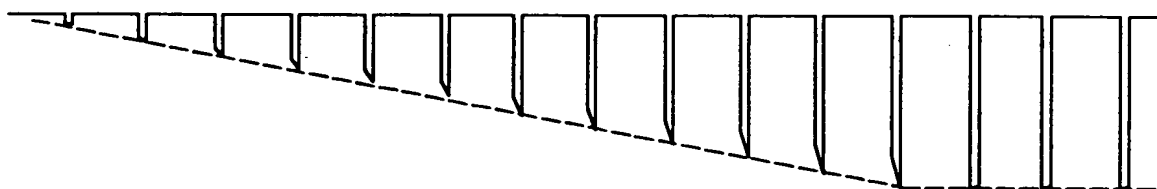
FIGURE 23

V'A'



LONG CHARGE UP TIME

V'B'



Ic1

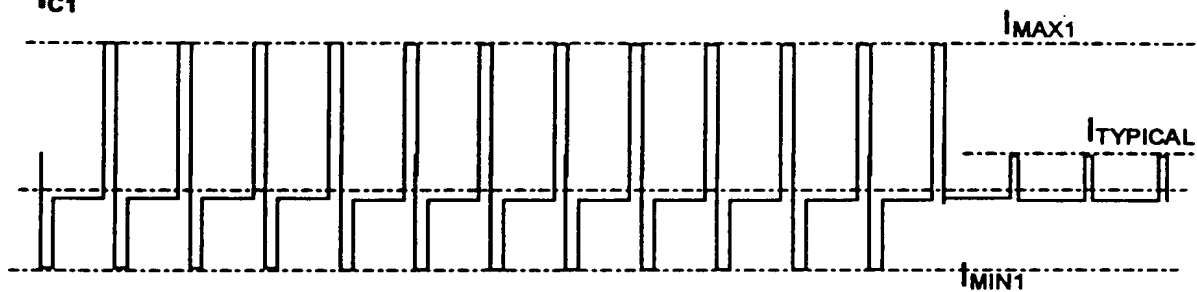
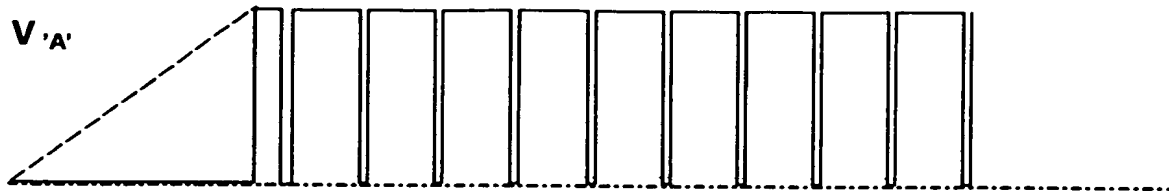


FIGURE 24

The diagram shows a circuit for processing a VBLANK signal. It starts with a **VBLANK INPUT** which is connected to a **VBLANK AMPLIFIER**. The output of the amplifier is connected to a **780a** (likely a 7805 regulator). The output of the 780a is connected to a **785** (likely a 7805 regulator). The output of the 785 is connected to a **784** (likely a 7804 regulator). The output of the 784 is connected to a **783** (likely a 7803 regulator). The output of the 783 is connected to a **786** (likely a 7806 regulator). The output of the 786 is connected to a **787** (likely a 7807 regulator). The output of the 787 is connected to a **788** (likely a 7808 regulator). The output of the 788 is connected to a **703vr** (likely a 703Vr regulator). The output of the 703vr is connected to a **705vb** (likely a 705Vb regulator). The output of the 705vb is connected to a **703vb** (likely a 703Vb regulator). The output of the 703vb is connected to a series of pulses.

FIGURE 25



→
SHORT START UP TIME

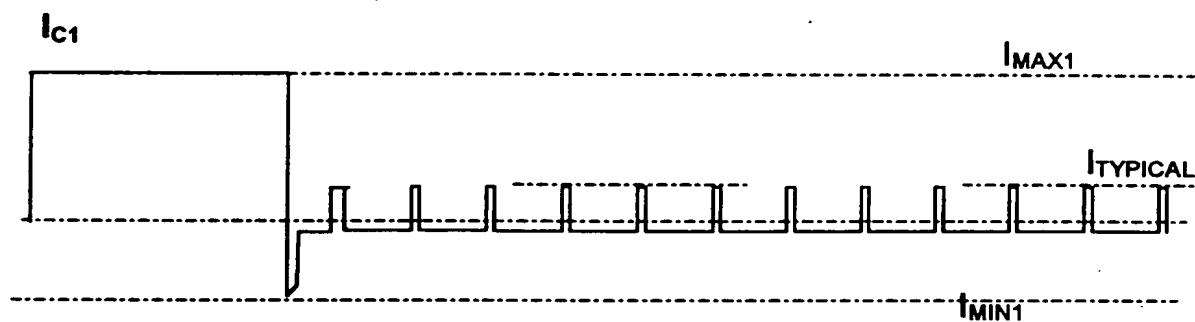
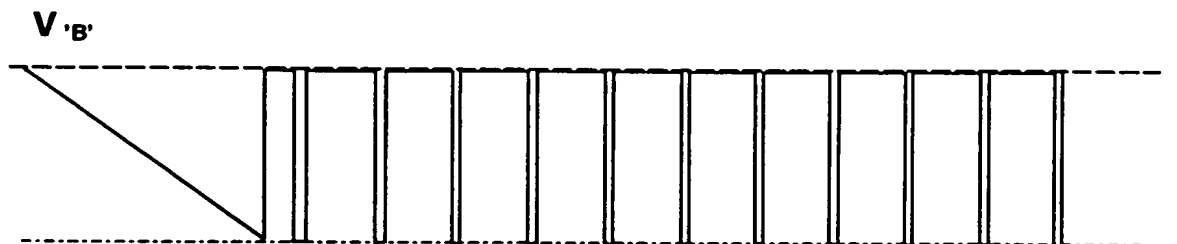
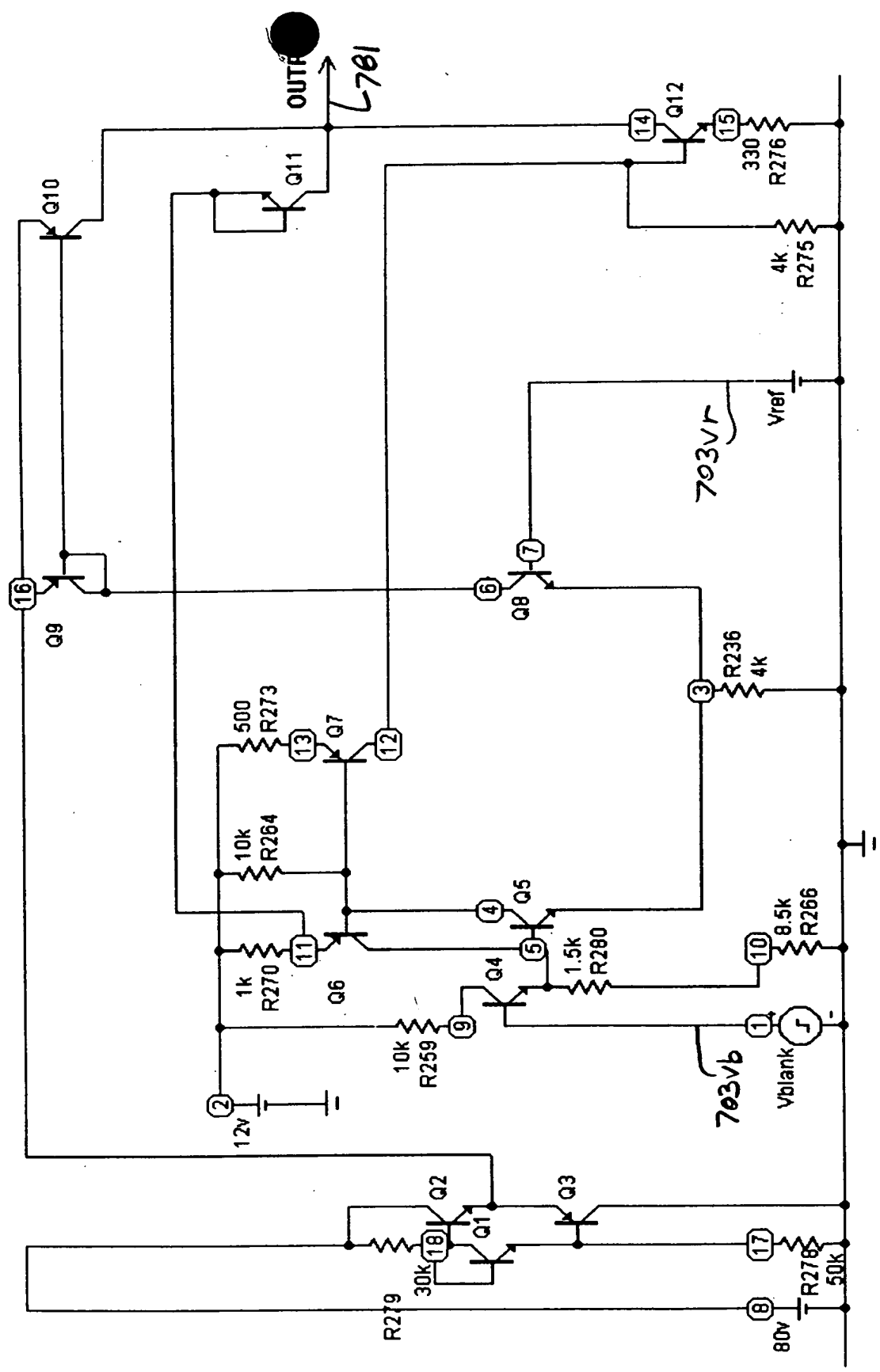


FIGURE 26

00/201" 6E/86560



700b

FIGURE 27

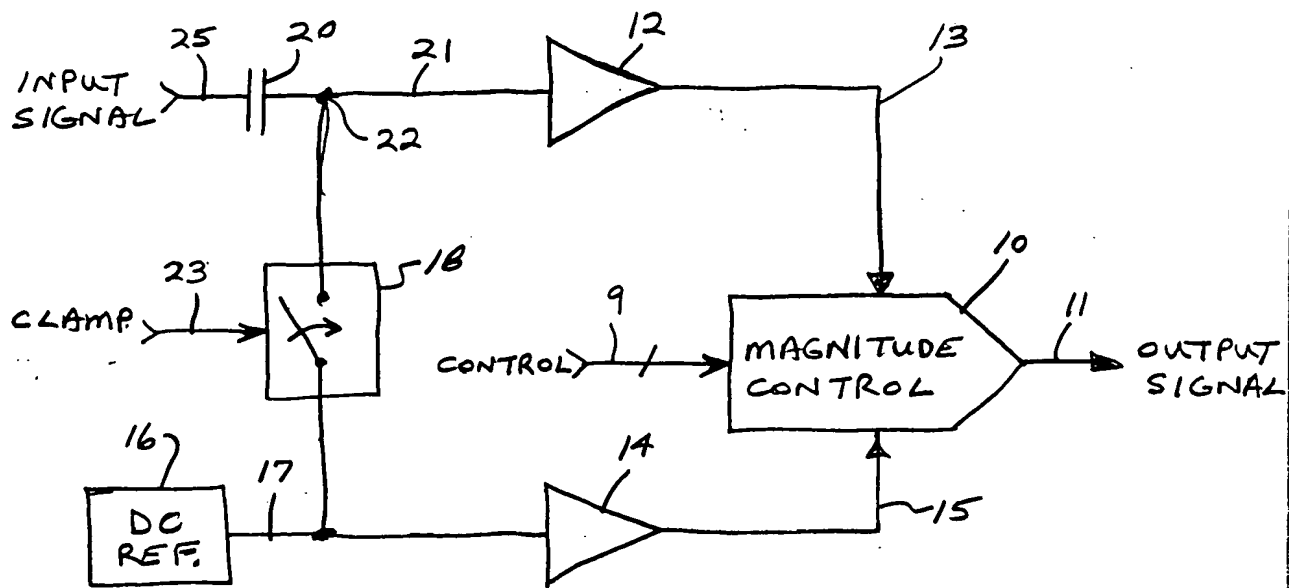


FIGURE 28

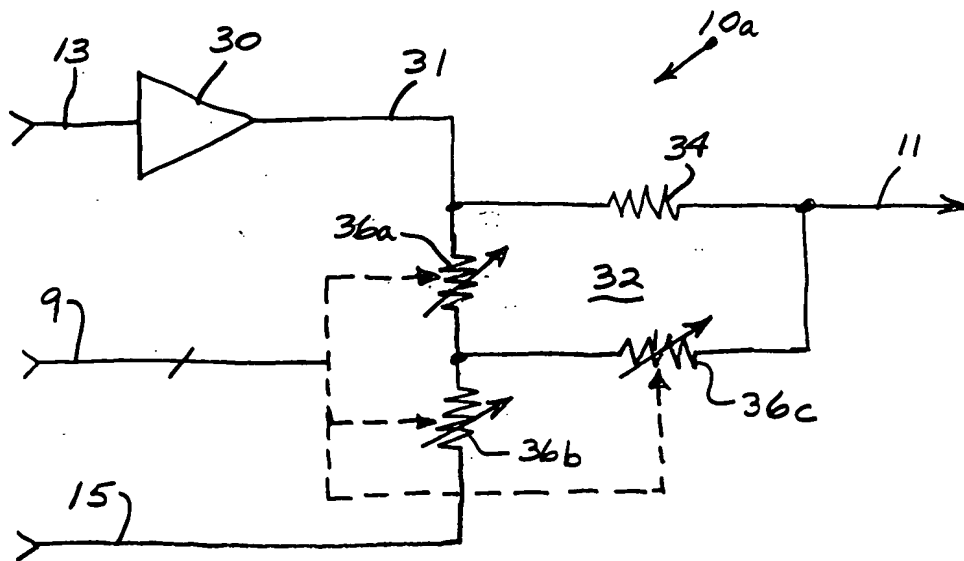


FIGURE 29

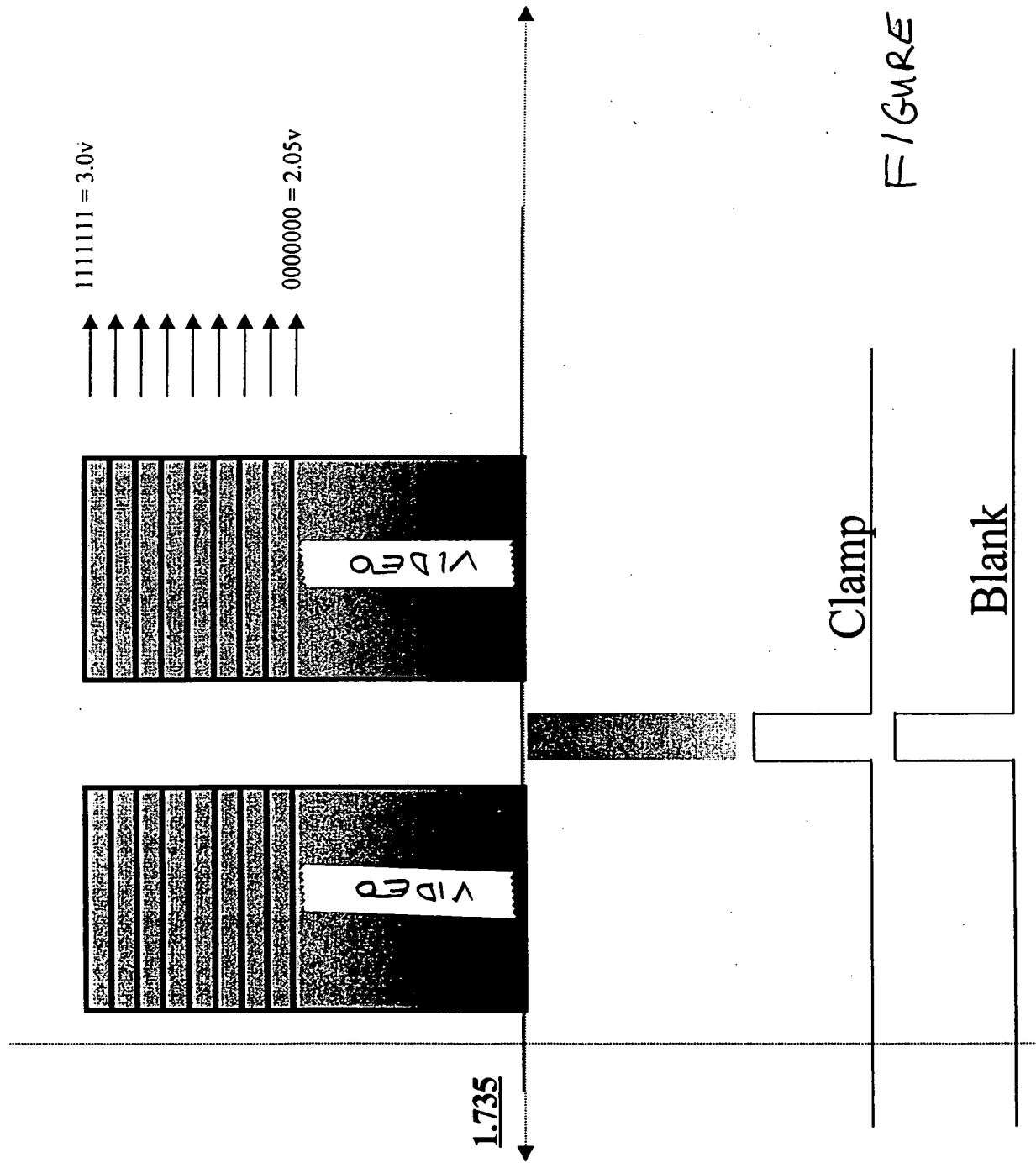


FIGURE 30

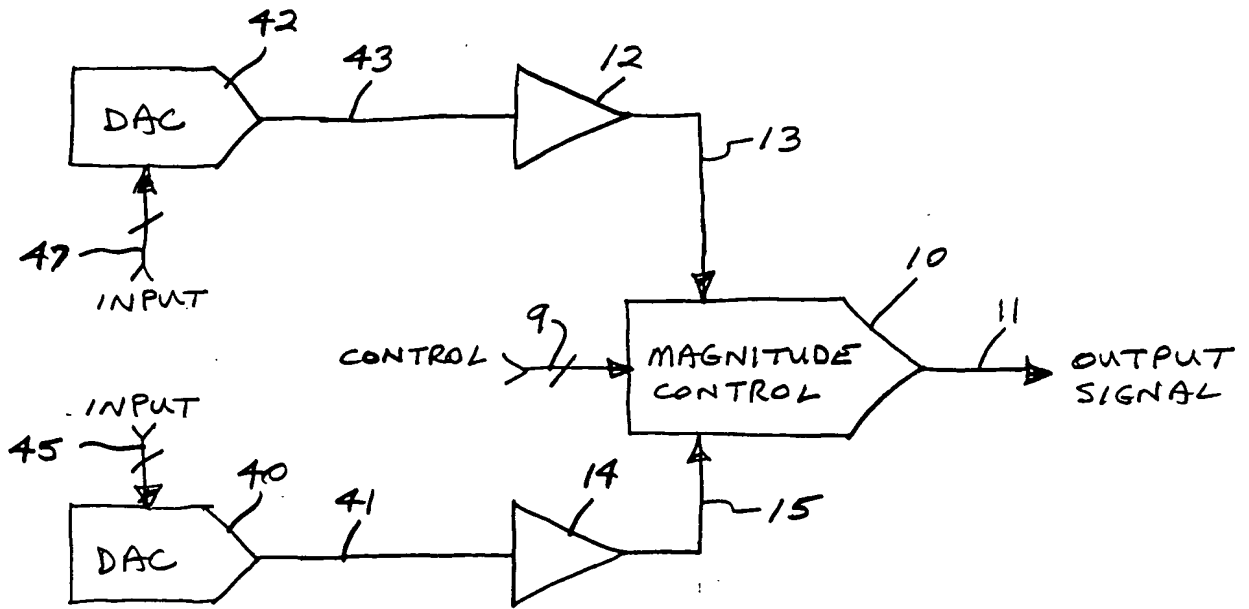


FIGURE 31

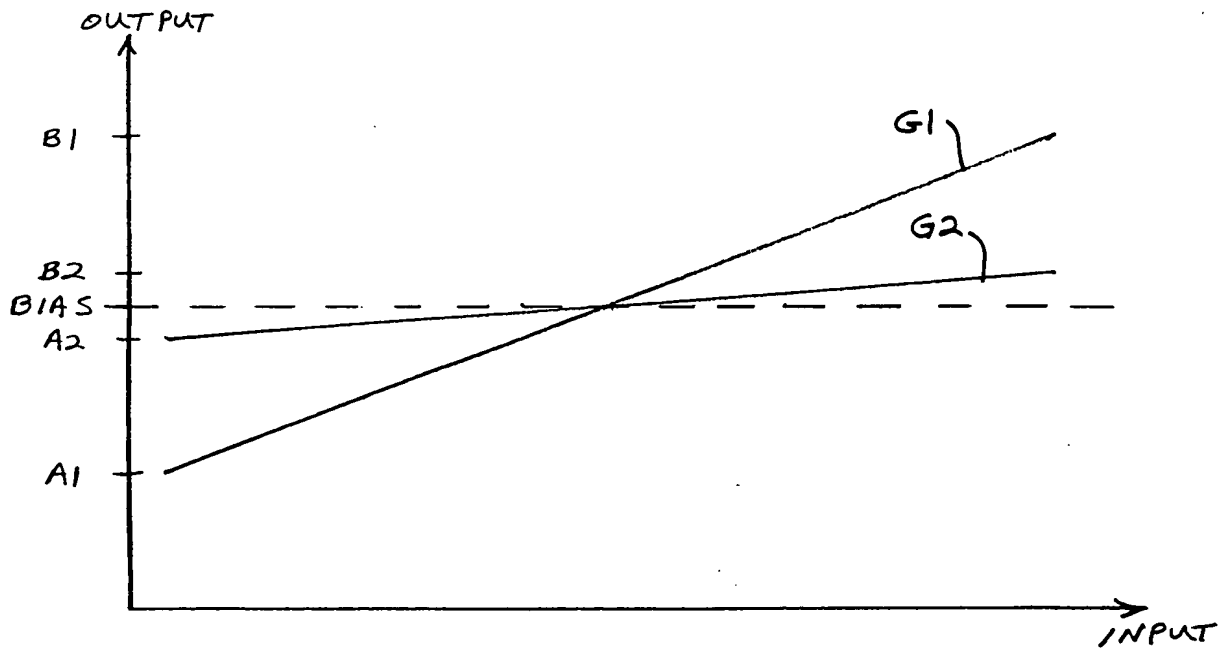


FIGURE 32

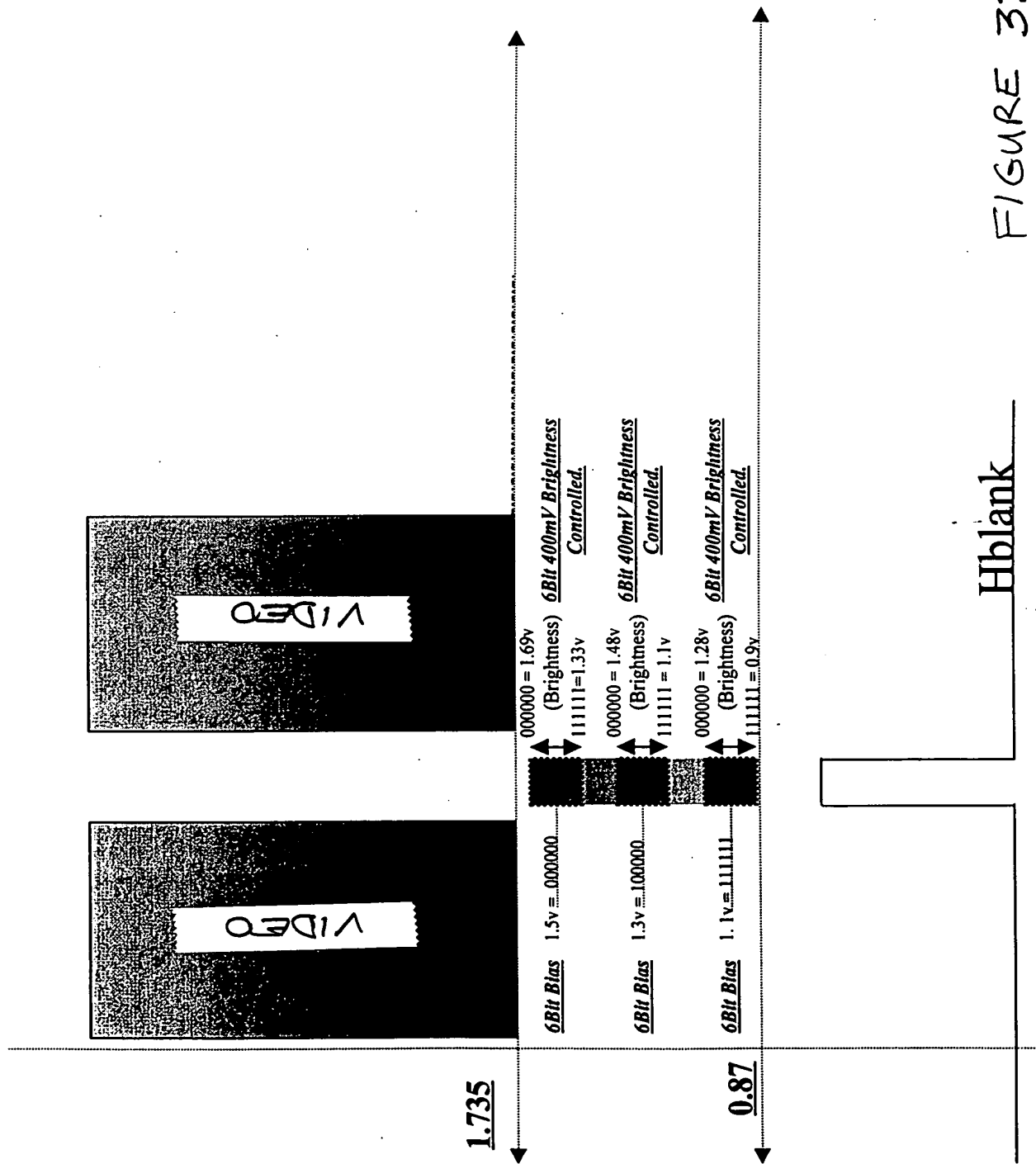


FIGURE 33

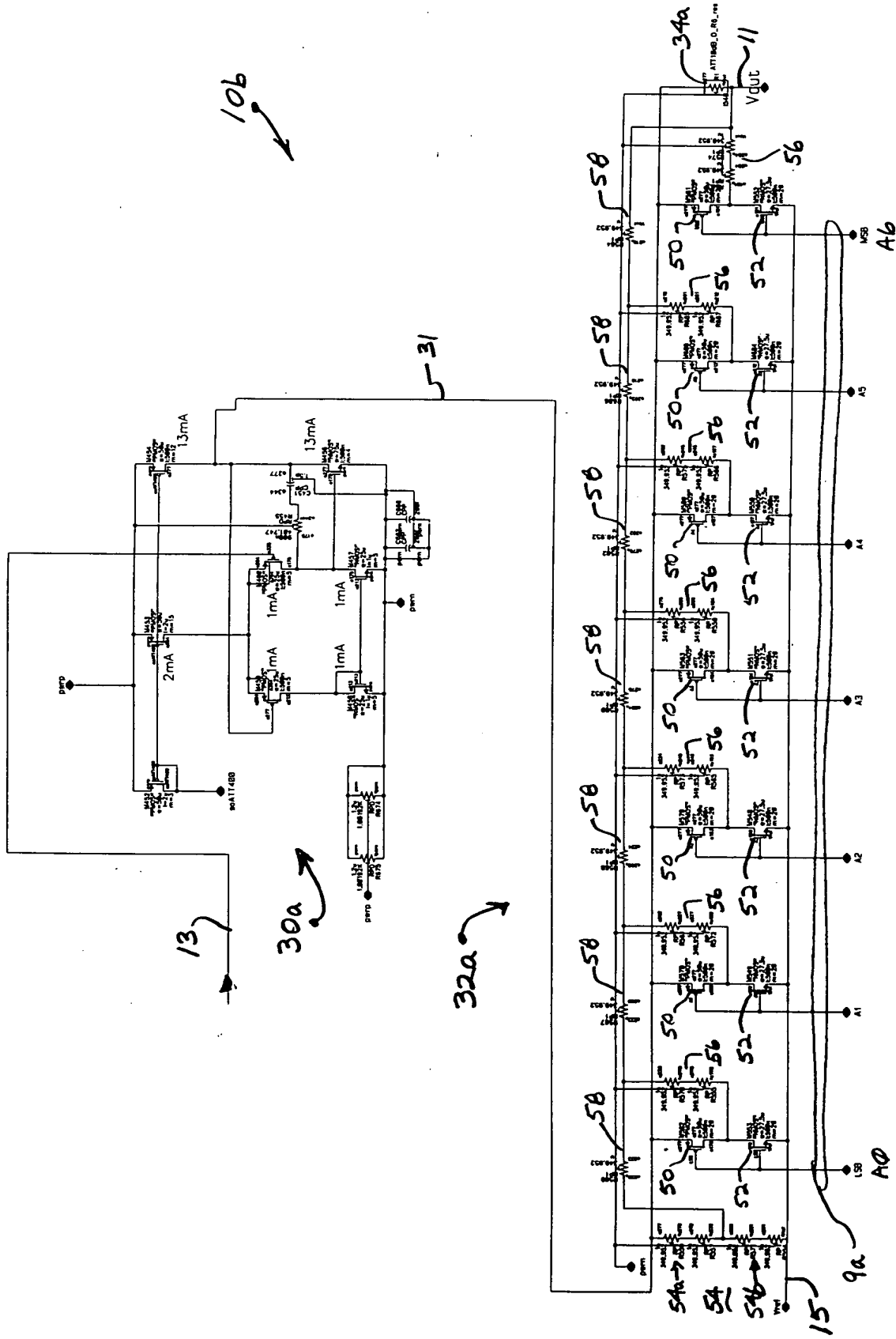


FIGURE 34

